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Built In Test For VLSI: Pseudorandom Techniques



Synopsis

This handbook provides ready access to all of the major concepts, techniques, problems, and solutions in the emerging field of pseudorandom pattern testing. Until now, the literature in this area has been widely scattered, and published work, written by professionals in several disciplines, has treated notation and mathematics in ways that vary from source to source. This book opens with a clear description of the shortcomings of conventional testing as applied to complex digital circuits, reviewing by comparison the principles of design for testability of more advanced digital technology. Offers in-depth discussions of test sequence generation and response data compression, including pseudorandom sequence generators; the mathematics of shift-register sequences and their potential for built-in testing. Also details random and memory testing and the problems of assessing the efficiency of such tests, and the limitations and practical concerns of built-in testing.

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Customer Reviews

This is the best test book I have read. It covers all aspects of BIST with emphasis on pseudorandom techniques. It does not cover as much about testing as "Digital System Testing and Testable Design" but it is well worth its price tag.

This is a great book written from professionals in the materia. I personally took the class with Dr. Savir and he is not only a great professor but this book is well worth purchasing.

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